MIPS Instructions

	op code	fnct code	
add rd, rs, rt	0x0	0x20	
addu rd, rs, rt	0x0	0x21	
addi rt, rs, imm	0x08	-	
addiu rt, rs, imm	0x09	-	
sub rd, rs, rt	0x0	0x22	
subu rd, rs, rt	0x0	0x23	
mul rd, rs, rt	-	-	
div rd, rs, rt	-	-	
rem rd, rs, rt	-	-	
and rd, rs, rt	0x0	0x24	
andi rt, rs, imm	0x0C	-	
or rd, rs, rt	0x0	0x25	
ori rt, rs, imm	0x0D	-	
nor rd, rs, rt	0x0	0x27	
xor rd, rs, rt	0x0	0x26	
xori rt, rs, imm	0x0E	-	
slt rd, rs, rt	0x0	0x2A	
sltu rd, rs, rt	0x0	0x2B	
slti rt, rs, imm	0x0A	-	
sltiu rt, rs, imm	0X0B	-	
sll rd, rt, shamt	0x0	0x0	shamt used, rs = 0
sllv rd, rt, rs	0x0	0x04	shamt = 0
srl rd, rt, shamt	0x0	0x02	shamt used, rs = 0
srlv rd, rt, rs	0x0	0x06	shamt = 0
lw rt, addr	0x23	-	
lh rt, addr	0x21	-	
lhu rt, addr	0x25	-	
lb rt, addr	0x20	-	
lbu rt, addr	0x24	-	
li rdest, imm	-	-	addi rdest, $zero, imm
la rdest, addr	-	-	
sw rt, addr	0x2B	-	
sh rt, addr	0x29	-	
sb rt, addr	0x28	-	
move rdest, rsrc	-	-	addu rdest, rsrc, $zero
beq rs, rt, label	0x04	-	
bgez rs, label	0x01	-	rt = 0x1
bgtz rs, label	0x07	-	rt = 0x0
blez rs, label	0x06	-	rt = 0x0
bltz rs, label	0x01	-	rt = 0x10
bne rs, rt, label	0x05	-	
j label	0x02	-	
jal label	0x03	-	
jr rs	0x0	0x08	rt = 0, rd = 0

Registers

$zero	0x0
$v0 - $v1	0x2 – 0x3
$a0 - $a3	0x4 – 0x7
$t0 - $t7	0x8 – 0xF
$s0 - $s7	0x10 – 0x17

Instruction Formats

R-Type	op	rs	rt	rd	0	fnct
I-Type	op	rs	rt	imm		
J-Type	op	address				